**Corrected Test Vectors Fall-18**

Resetin=1

Resetin=0

Clk 2ns

H = 0

H = 1

1. X2-X0-X4 Reset state
2. X2-X0-X3-X4 Reset state
3. X2-X0-X3-X3-X4 Reset state
4. X2-X4 Reset state
5. X2-X0-X3-X3-X1-X4 Lock state
6. X2-X2-X2 Lock state
7. X2-X0-X2-X2 Lock state
8. X2-X0-X3-X2-X2 Lock state
9. X2-X0-X3-X3-X2-X2 Lock state
10. X2-X0-X3-X3-X1-X2-X2 Lock state
11. X2-X4 Lock state
12. X2-X0-X4 Lock state
13. X2-X0-X3-X4 Lock state
14. X2-X0-X3-X3-X4 Lock state
15. X2-X1-X3-X3-X1-X4 Alarm state
16. X2-X0-X3-X3-X1-X4 Reset state
17. X2-X0-X3-X3-X1-X4 Lock state
18. X2-X0-X2-X3-X2-X4 Alarm state
19. X2-X0-X3-X3-X1-X4 Reset state
20. X2-X0-X3-X3-X1-X4 Lock state
21. X2-X0-X3-X3-X1-X0 Alarm state
22. X2-X0-X3-X3-X1-X4 Reset state
23. X2-X0-X3-X3-X1-X4 Lock state
24. X2-X0-X3-X3-X1-X2-X3 Alarm state
25. X2-X0-X3-X3-X1-X4 Reset state
26. X2-X0-X3-X3-X1-X4 Lock state
27. X2-X0-X3-X3-X1-X2-X4 Alarm state
28. X2-X0-X3-X3-X1-X4 Reset state
29. X2-X0-X3-X3-X1-X4 Lock state
30. X2-X1-X3-X0-X1-X4 Alarm state
31. X2-X0-X3-X3-X1-X4 Reset state
32. X2-X0-X3-X3-X1-X4 Lock state
33. X1-X0-X3-X3-X1-X4 Alarm state
34. X3  Alarm state
35. X2-X2 Alarm state
36. X2-X1 Alarm state
37. X2-X0-X2 Alarm state
38. X2-X0-X3-X4 Alarm state
39. X2-X0-X3-X3-X4 Alarm state
40. X2-X0-X3-X3-X2 Alarm state
41. X2-X0-X3-X3-X1-X4 Reset state

H = 0

1. X2 Reset state
2. X3  Reset state

H = 1

1. X2-X0-X3

H = 0 Reset state

H = 1

1. X2-X0-X3-X3-X1

H = 0 Reset state

H = 1

1. X2-X0-X3-X3-X1-X4 Lock state

H = 0 Alarm state

1. X2-X0-X3-X3-X1-X4 Reset state

H = 1

1. X2-X0-X3-X3-X1-X4 Lock state
2. X2-X0-X3-X3 Unlocking process

H= 0 Alarm state